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L Number	Hits	Search Text	DB	Time stamp
1	390	(715/503,504,509).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 11:54
2	53572	(creat\$3 generat\$3) near3 (spreadsheet table (spread adj1 sheet))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 12:28
3	12106	(defin\$3 specif\$4) with number with (row column)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 11:56
4	46	((715/503,504,509).CCLS.) and ((creat\$3 generat\$3) near3 (spreadsheet table (spread adj1 sheet))) and ((defin\$3 specif\$4) with number with (row column))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 12:25
5	233792	(design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:53
6	60013	(creat\$3 develop\$4 generat\$3) near3 (spreadsheet table (spread adj1 sheet))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:45
7	204	((defin\$3 specif\$4) with number with (row column)) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet table (spread adj1 sheet)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 12:29
8	204	((defin\$3 specif\$4) with number with (row column)) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet table (spread adj1 sheet))) not (((715/503,504,509).CCLS.) and ((creat\$3 generat\$3) near3 (spreadsheet table (spread adj1 sheet))) and ((defin\$3 specif\$4) with number with (row column)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:39
9	4305	data adj1 driven	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:39
10	139	(data adj1 driven) with (grid table spreadsheet matrix (spread adj1 sheet))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:46
11	13	((design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((data adj1 driven) with (grid table spreadsheet matrix (spread adj1 sheet)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:40
12	96633	(creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:45
13	36010	data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:47

14	1003	((design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:47
15	15102	cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:50
16	85	(((design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet)))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:51
17	8	((defin\$3 specif\$4) with number with (row column)) and (((design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet)))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:51
18	547979	(design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:54
19	8	((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 15:03
20	13	((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:59

21	5	((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet)) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) not ((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet)) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:59
22	145	((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet)) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 15:03
23	85	((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet)) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 15:03

24	85	((data adj1 driven) and (((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit)))) not (((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) (((defin\$3 specif\$4) with number with (row column)) and (((design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet)))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list))))) (((design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((data adj1 driven) with (grid table spreadsheet matrix (spread adj1 sheet)))))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 15:08
25	146	((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit)))) not23	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 15:08

26	60	((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet))) and ((design\$3 manufatur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) not (((data adj1 driven) and (((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet))) and ((design\$3 manufatur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit)))) not (((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list))) and ((design\$3 manufatur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit)))) (((defin\$3 specif\$4) with number with (row column)) and (((design\$3 manufatur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list))))) (((design\$3 manufatur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((data adj1 driven) with (grid table spreadsheet matrix (spread adj1 sheet)))))))	USPAT; US - PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 15:09
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